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(to be used for all correspondence after initial filing)

Application Number	09/997,030
Filing Date	11/30/2001
First Named Inventor	Volodymyr V. Kindratenko ET AL.
Art Unit	2671
Examiner Name	Enrique L. Santiago
Attorney Docket Number	00-444

Total Number of Pages in This Submission

ENCLOSURES (Check all that apply)

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Request for Certificate of Correction |
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Remarks

Certificate
SEP 16 2005
of Correction

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Andrew J. Ririe, Registration No. 45,597
Signature	
Date	September 8, 2005

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope address to:
Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on this date: 09/08/2005

Typed or printed name	Andrew J. Ririe
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Date 8-Sep-2005

This collection of information is required by 37 CFR 1.17 and 1.27. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 37 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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SEP 19 2005



PATENT

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re United States Letters) Group Art Unit: 2671
)
Patent No.: 6,897,863 *B2*) Examiner: Enrique L. Santiago
)
Granted: May 24, 2005)
)
Inventor: Volodymyr V. Kindratenko et al.)
)
Title: SYSTEM AND METHOD FOR)
HIDDEN OBJECT REMOVAL)
)
Attorney Docket No.: 00-444)

Peoria, Illinois 61629-6490
September 8, 2005

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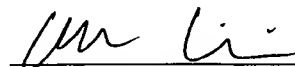
REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 CFR §1.322

Sir:

The patentee, Caterpillar Inc., of the above-referenced patent requests the issuance of a Certificate of Correction under 37 C.F.R. § 1.322. The mistakes were made by the Patent and Trademark Office. In particular, the amendment of October 20, 2004, was not reflected in the version of the claims included in the issued patent.

Two copies of Form PTO-1050 are submitted herewith on the desired corrections.

Respectfully submitted,



Andrew J. Ririe
Registration No. 45,597
Caterpillar Inc.

Telephone: (309) 636-1974
AJR:dmc
Enc.

SEP 19 2005

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO.: 6,897,863 B2
APPLICATION NO.: 09/997,030
ISSUE DATE: May 24, 2005
INVENTOR(S): Volodymyr V. Kindratenko et al.

Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Please correct the Claims as follows:

Claim 1, Column 10, Line 44, delete "hidden" and insert --occluded--

Claim 1, Column 10, Line 56, delete "and"

Claim 1, Column 10, Line 60, delete "grid." and insert --grid; and
identifying at least one occluded portion of the image based on the identified second
plurality of n-dimensional components.--

Claim 3, Column 10, Line 64, after "components" delete "." and insert --and excluding the
at least one occluded portion.--

Claim 4, Column 10, Line 65, delete "The method of claim 1, wherein said identifying
further includes:" and insert --A computer-implemented method for identifying hidden and
visible surfaces on an n-dimensional object, wherein n is greater than 1, said method
comprising:

generating an n-dimensional image of an object, said image including a first plurality of
n-dimensional components that define a shape and orientation of the image and a
plurality of parts located inside the image;
superimposing an n-dimensional grid of pixels on said image, said pixels arranged in a
lattice structure such that each pixel of said grid corresponds to one of a plurality of
vertices of an m-sided cell, wherein each side of said m-sided cell includes at least four
vertices; and
identifying a second plurality of n-dimensional components located on an outer perimeter
of said image that abut or overlap at least one side of one m-sided cell in said grid,
wherein said identifying includes:--

MAILING ADDRESS OF SENDER:

Caterpillar Inc.
100 NE Adams St.
Peoria, IL 61629-6490

This collection of information is required by 37 CFR 1.322, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO.: 6,897,863 *B2*
APPLICATION NO.: 09/997,030
ISSUE DATE: May 24, 2005
INVENTOR(S): Volodymyr V. Kindratenko et al.

Page 2 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 10, Column 11, Line 38, delete "hidden" and insert --occluded--

Claim 10, Column 11, Line 53, delete "and"

Claim 10, Column 11, Line 57, delete "grid." and insert --grid; and
a module identifying at least one occluded portion of the image based on the identified
second plurality of n-dimensional components.--

Claim 12, Column 11, Line 61, delete "components." and insert --components and
excluding the at least one occluded portion.--

Claim 13, Column 11, Line 62, delete "The apparatus of claim 10, wherein said module for
identifying further includes the capability to:" and insert --An apparatus for identifying
hidden and visible surfaces on an n-dimensional object, wherein n is greater than 1, said
apparatus comprising:

- a network device having a memory containing a program that further includes:
- a module for generating an n-dimensional image of an object, said image including a first plurality of n-dimensional components that define a shape and orientation of the image and a plurality of parts located inside the image;
- a module for superimposing an n-dimensional grid of pixels on said image, said pixels arranged in a lattice structure such that each pixel of said grid corresponds to one of a plurality of vertices of an m-sided cell, wherein each side of said m-sided cell includes of at least four vertices; and
- a module for identifying a second plurality of n-dimensional components located on an outer perimeter of said image that abut or overlap at least one side of one m-sided cell in said grid, wherein said module for identifying further includes the capability to:--

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Peoria, IL 61629-6490

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.: 6,897,863 *B2*
APPLICATION NO.: 09/997,030
ISSUE DATE: May 24, 2005
INVENTOR(S): Volodymyr V. Kindratenko et al.

Page 3 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 19, Column 12, Line 53, delete "and"

Claim 19, Column 12, Line 57, delete "grid." and insert --grid; and
identifying at least one hidden portion of the image based on the identified second
plurality of n-dimensional components.--

Claim 21, Column 12, Line 62, delete "components." and insert --components, excluding
the at least one hidden portion.--

Claim 22, Column 12, Line 63, delete "The" and insert --A--

Claim 22, Column 12, Line 63, delete "of claim 19, wherein said identifying further
includes:" and insert --having stored thereon machine executable instructions, the execution
of said instructions adapted to implement a method for identifying hidden and visible
surfaces on an n-dimensional object, wherein n is greater than 1, said method comprising:
generating an n-dimensional image of an object, said image including a first plurality of
n-dimensional components that define a shape and orientation of the image and a
plurality of parts located inside the image;
superimposing an n-dimensional grid of pixels on said image, said pixels arranged in
a lattice structure such that each pixel of said grid corresponds to one of a plurality of
vertices in an m-sided cell, wherein each side of said m-sided cell includes at least
four vertices; and
identifying a second plurality of n-dimensional components located on an outer perimeter
of said image that abut or overlap at least one side of one m-sided cell in said grid,
wherein said identifying includes:--

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This collection of information is required by 37 CFR 1.322, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

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